



# DEVELOP SMART COMPUTER VISION SOLUTIONS FASTER

**WITH INTEL<sup>®</sup> COMPUTER VISION SDK &  
OTHER ADVANCED SOFTWARE TOOLS**

Hosted by the Embedded Vision Alliance  
Presented by Intel Corporation

# AGENDA

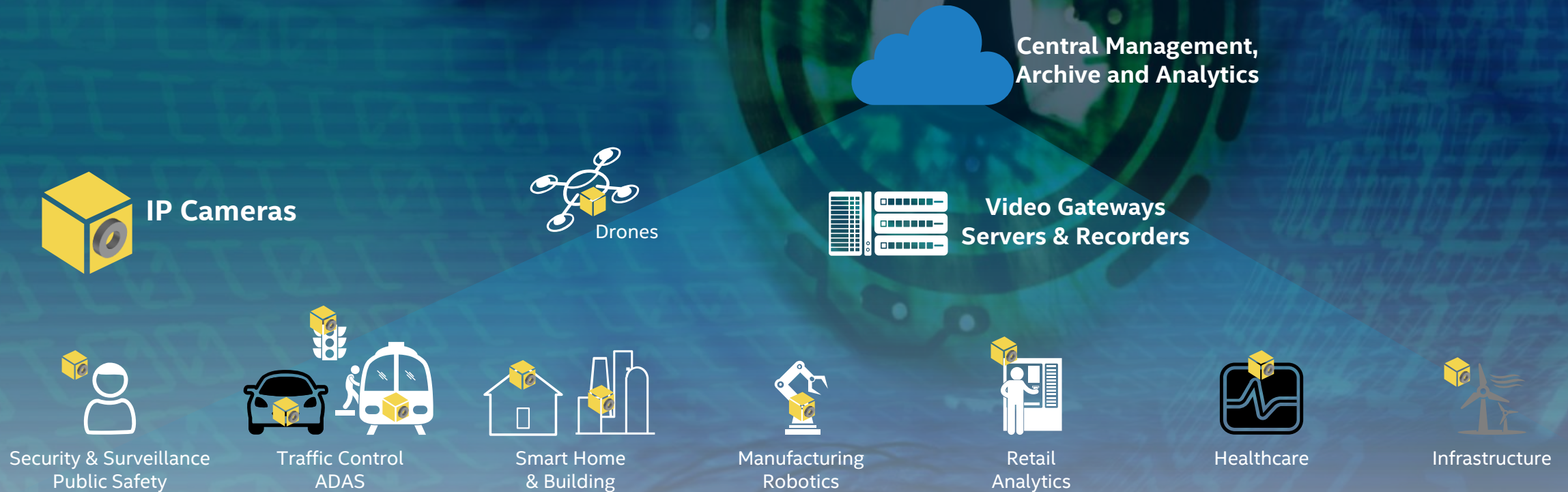
1. Trends Driving a Need for Computer Vision
2. Computer Vision & Deep Learning – Value Together
3. Optimize Your Applications with the Right Tools
  - Intel® System Studio
  - Intel® Computer Vision SDK
  - Intel® Media SDK
  - Intel® SDK for OpenCL™ Applications

## Goals

- Show how integrating computer vision can bring smart capabilities to great solutions
- Provide a technical introduction to each so you can get started

# VIDEO: THE NEW FRONTIER

Multiple sources: IHS, Markets & Markets, Strategy Analytics, Intel research



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# CONNECTED DEVICES ARE EVERYWHERE

And Video Use is Increasing



## Developers need tools that...

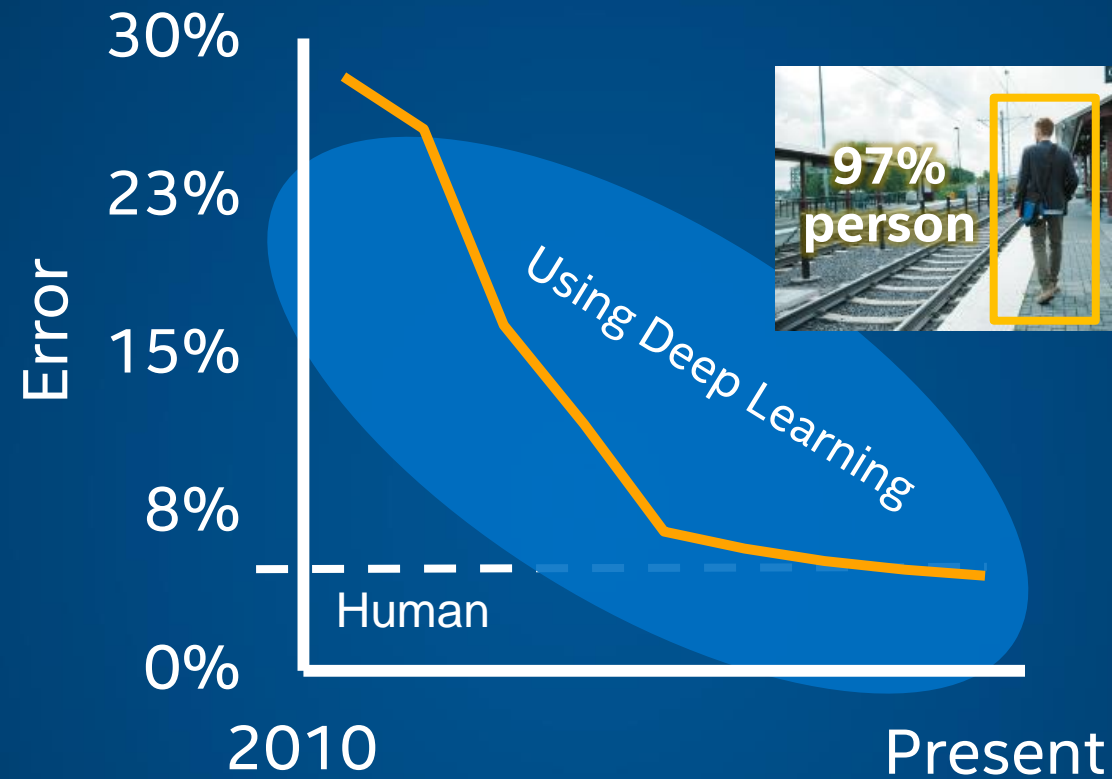
- Are comprehensive and easy to use
- Quickly help resolve defects in complex systems
- Offer insight into sources of excess power consumption
- Enable & accelerate performance - demanding & unique, competitive use cases

**...and take full advantage of Intel hardware accelerators**

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# DEEP LEARNING BREAKTHROUGH

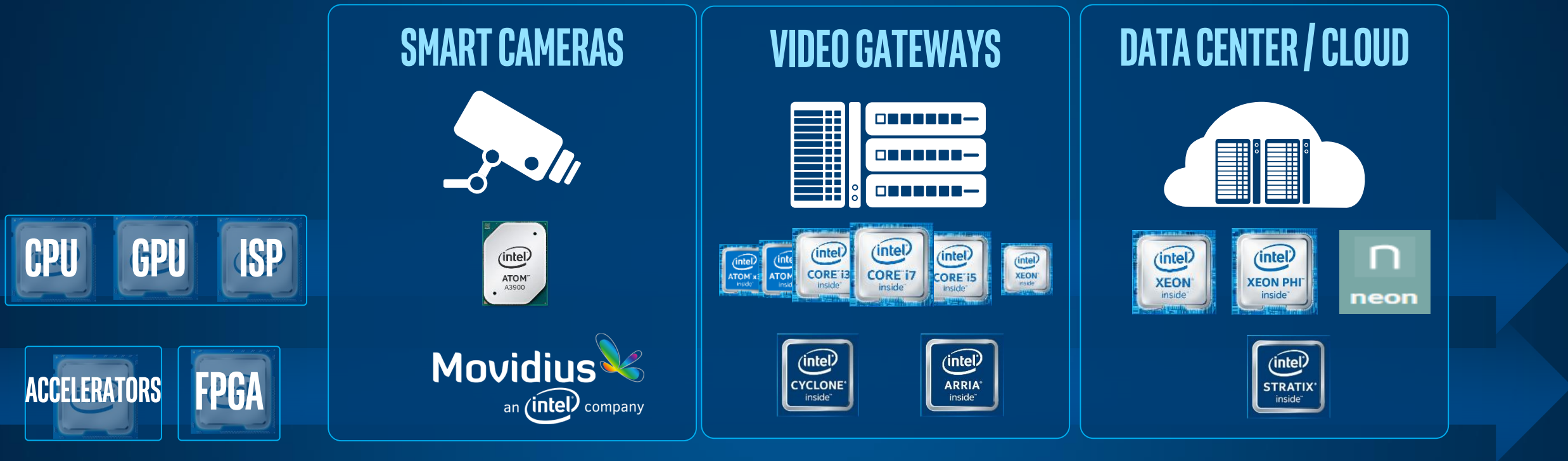


## Image Recognition

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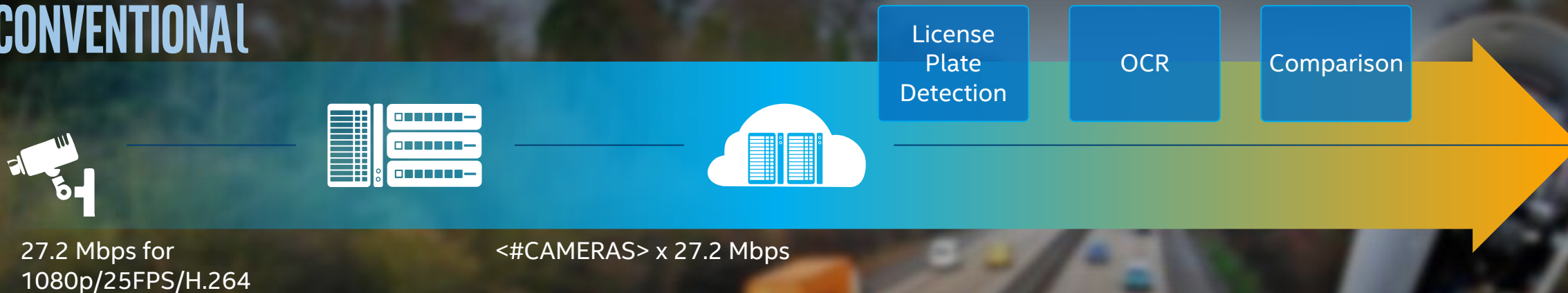
← **ACCELERATE & DIFFERENTIATE WITH INTEL SOFTWARE TOOLS** →

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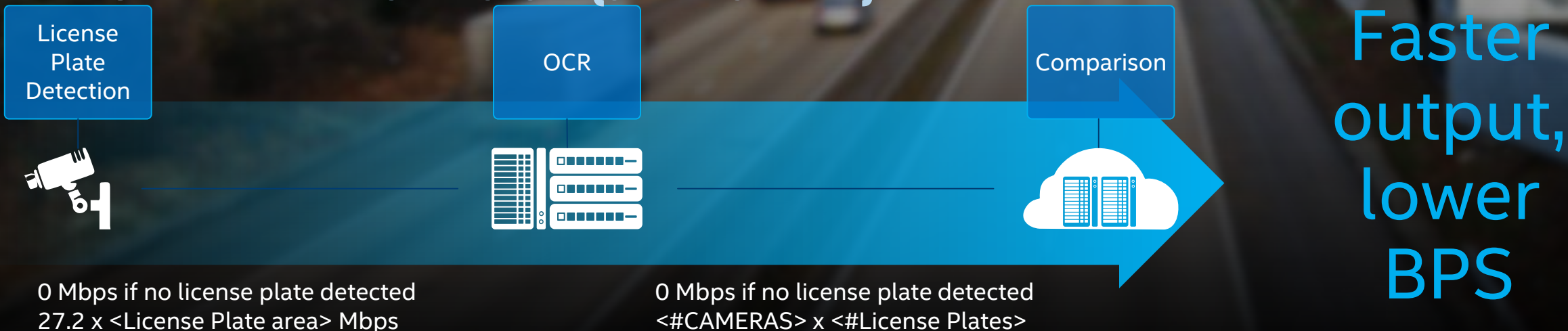
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## CONVENTIONAL



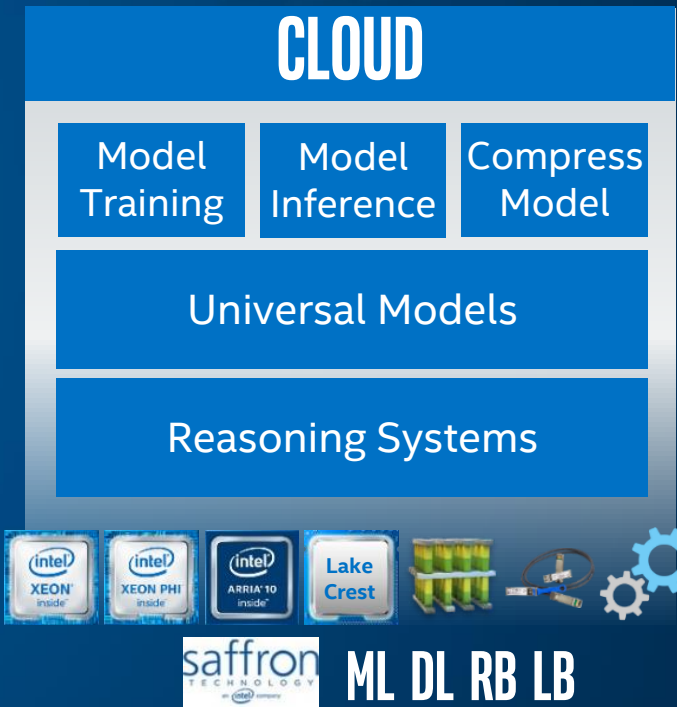
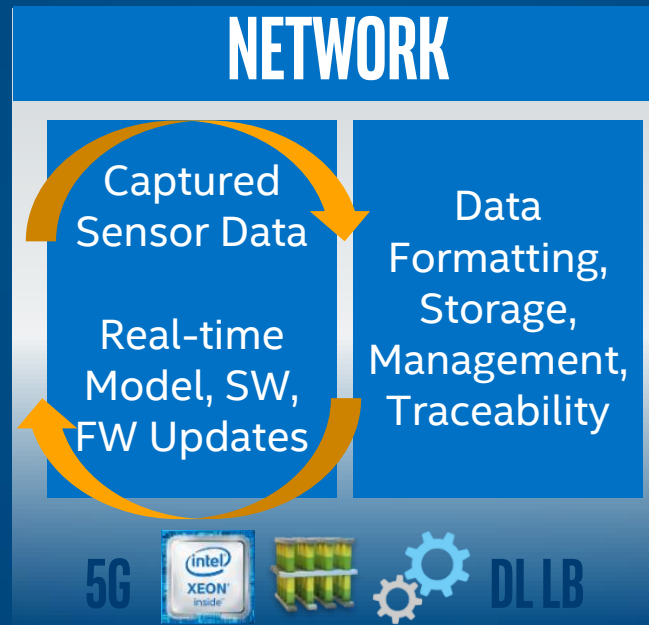
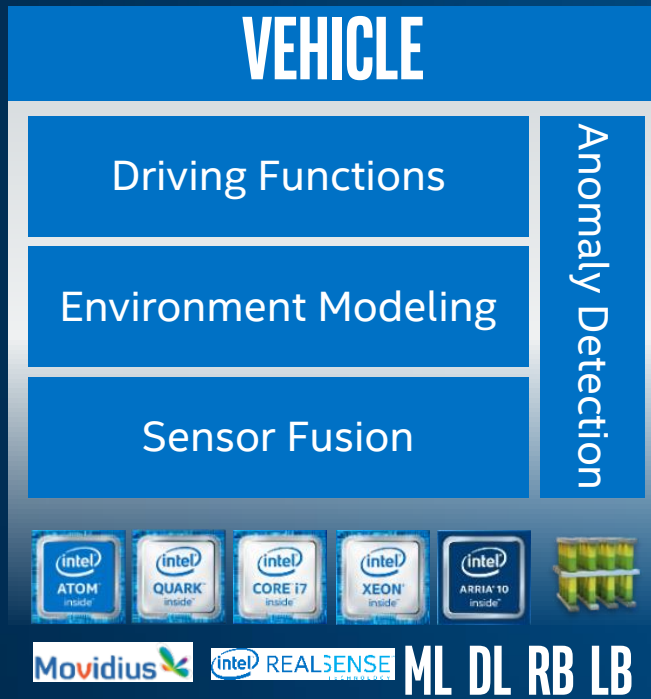
## END-TO-END INTEL<sup>®</sup> ARCHITECTURE (SMART CAMERA)



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# END-TO-END ARTIFICIAL INTELLIGENCE FOR AUTOMATED DRIVING



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# INTEL COMPUTER VISION PORTFOLIO

## EXPERIENCES



## TOOLS



Intel® System Studio



Intel® Media SDK



Intel® Computer Vision SDK

Intel® SDK for OpenCL™ Applications



## FRAMEWORKS



theano



Caffe

## LIBRARIES

Intel® Data Analytics Acceleration Library

Intel® Distribution for python  
Intel® Math Kernel Library



Intel® Nervana™ Graph\*

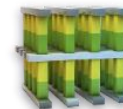
Movidius Stack



## HARDWARE



Compute



Memory & Storage



Networking



Visual Intelligence



UNLEASH  
FULL  
POTENTIAL

\*Coming 2017

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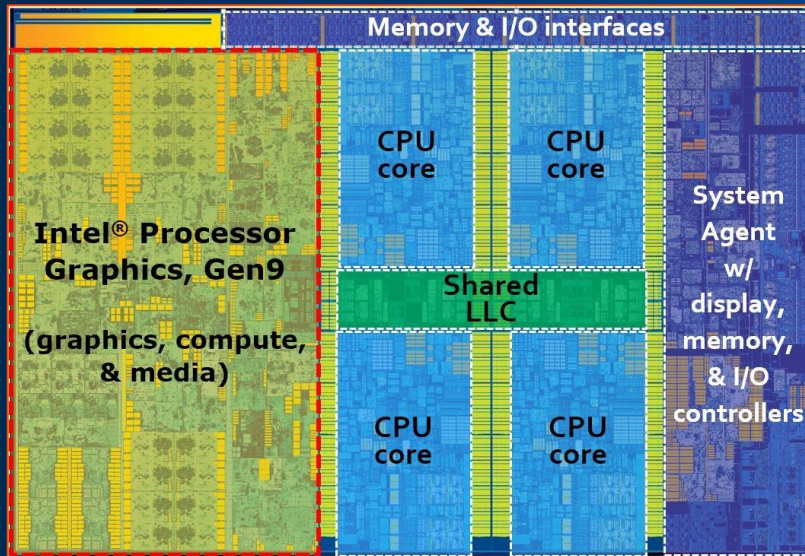
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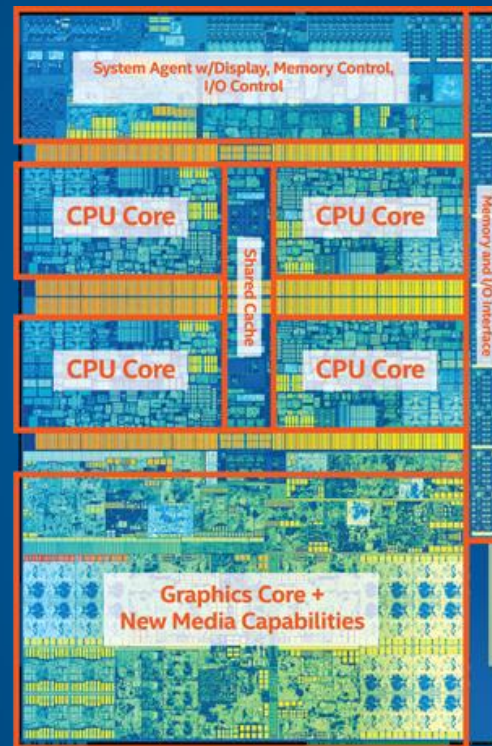
# INTEL HARDWARE IS HETEROGENEOUS

## Skylake



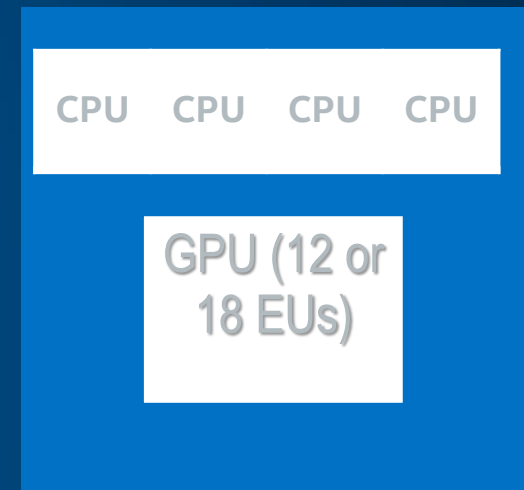
6<sup>th</sup> Generation Core

## Kabylake



7<sup>th</sup> Generation Core

## Apollo Lake



Intel® Pentium

- J4205, J4200

Intel® Celeron

- J3455, J3355
- N3350, N3450

Intel Atom

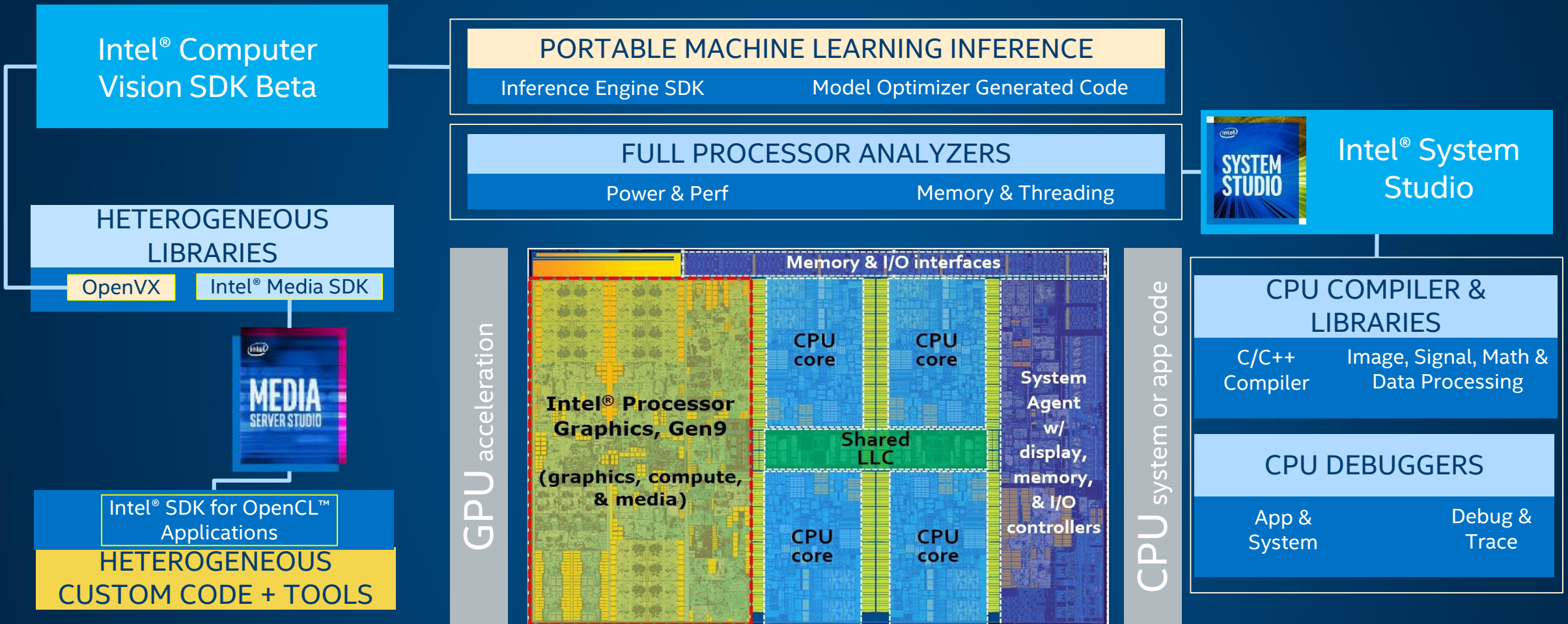
- E3950, E3940, E3930

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# INTEL® SYSTEM STUDIO + HETEROGENEOUS SDKS

Deep System-wide Insight - Unlock Performance for System, Embedded & IoT Developers



**Better Together: A Portfolio to use Full Processor Capabilities**

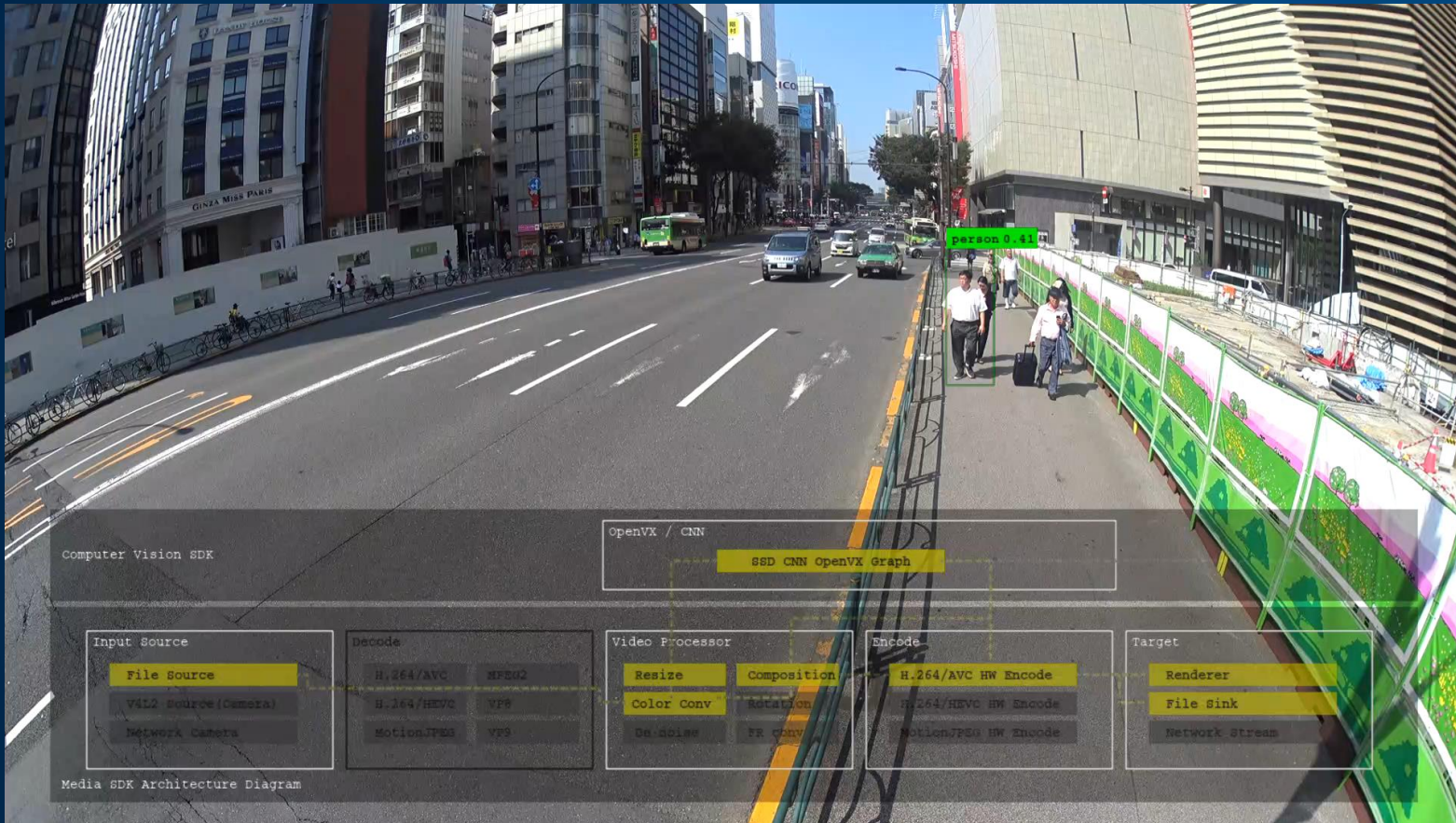
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# UNLOCK HARDWARE CAPABILITIES



**NEW!**

**INTEGRATE VISUAL UNDERSTANDING**  
Intel® Computer Vision SDK Beta  
Linux\*/Yocto\* version available

**HETEROGENEOUS CUSTOM DEVELOPMENT**  
Intel® SDK for OpenCL™  
Application Development

**ACCELERATE VIDEO PROCESSING**  
Intel® Media SDK  
for Embedded Linux\*, Windows\* & Open Source

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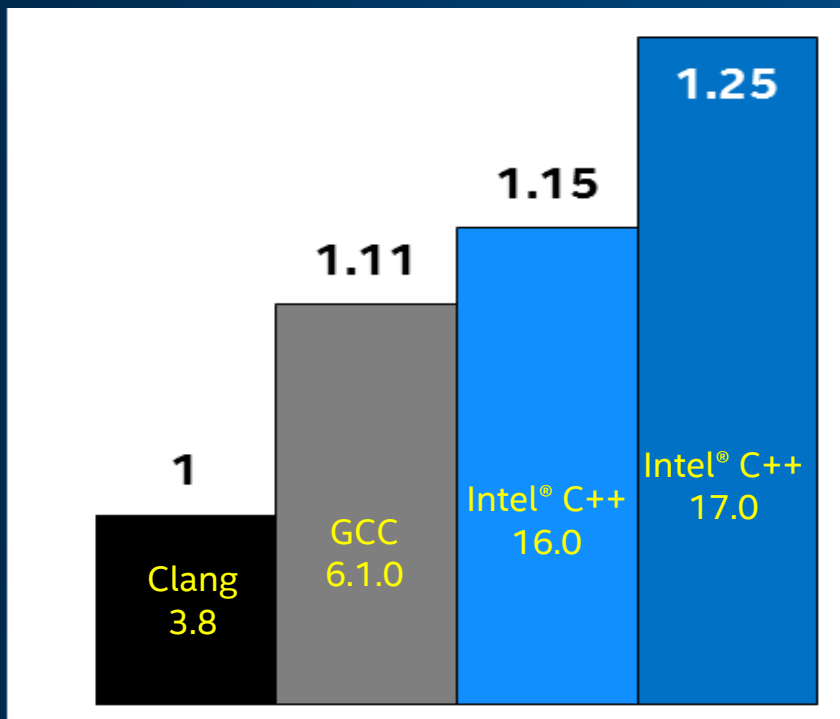
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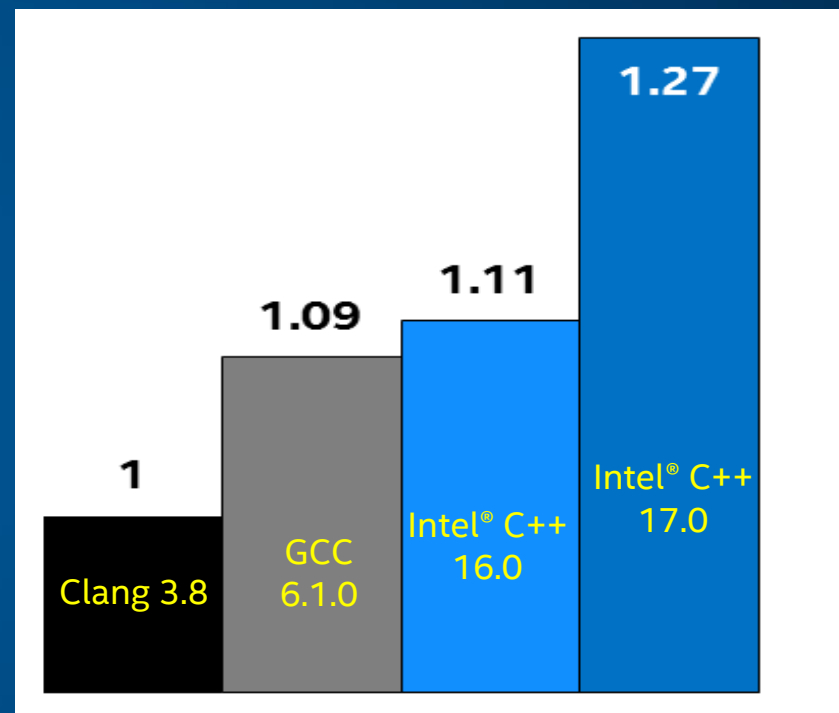
# INTEL® C++ COMPILERS DELIVER IMPRESSIVE PERFORMANCE ON EMBEDDED APPLICATIONS POWERED BY INTEL ATOM PROCESSORS

Coremark Pro\* benchmarks running on Intel Atom processors

32-bit mode



64-bit mode



Configuration: Intel® Atom™ CPU C2750 @ 2.41GHz. Software: Intel® C++ Compiler 17.0, Intel C++ compiler 16.0, GCC 6.1.0, Clang/LLVM 3.8. Linux OS: Red Hat Enterprise Linux\* 7.0, Kernel 3.10.0-123.el7.x86\_64, 32GB RAM. Coremark Pro\* Benchmark ([www.eembc.org](http://www.eembc.org)). Compiler flags: Intel C++ 17.0: -O3 -ipo -no-prec-div -ansi-alias -xATOM\_SSE4.2 -static; Intel C++ 16.0: -O3 -ipo -no-prec-div -ansi-alias -xATOM\_SSE4.2 -static; GCC 6.1.0: -Ofast -mfpmath=sse -ftto -march=native -funroll-loops -ffat-lto-objects; LLVM 3.8: -Ofast -mfpmath=sse -ftto -march=native -funroll-loops -static. GCC and clang/LLVM 32bit compilers have additional flag -m32. Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark & MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information & performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. Benchmark Source: Intel Corporation - **Optimization Notice:** Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets & other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User & Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804.

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# YOUR BUILDING BLOCKS FOR IMAGE, SIGNAL & DATA PROCESSING APPS

Intel® Integrated Performance Primitives (Intel® IPP)

## What is Intel® IPP?

Provides developers with ready-to-use, processor-optimized functions to accelerate **Image, Signal, Data Processing & Cryptography** computation tasks

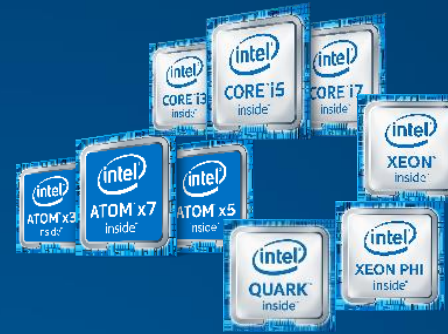
## Why use Intel® IPP?

- High Performance
- Easy to use API's
- Faster Time To Market
- Production Ready

## How to get Intel® IPP

[Intel System Studio](#)  
[Intel Parallel Studio XE](#)  
[Free Tools Program](#)

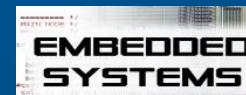
Optimized for



Supports



Addresses



## Image Processing

- Medical Imaging
- Computer Vision
- Digital Surveillance
- Biometric Identification
- Automated Sorting
- ADAS
- Visual Search

## Signal Processing

- Games (sophisticated audio content or effects)
- Echo cancellation
- Telecommunications
- Energy

## Data Compression & Cryptography

- Data centers
- Enterprise data Managements
- ID verification
- Smart Cards/wallets
- Electronic Signature
- Informationsecurity/cybersecurity

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Find out more at: [software.intel.com/intel-ipp](http://software.intel.com/intel-ipp)

Contact us: [software.intel.com/en-us/forums/intel-integrated-performance-primitives](http://software.intel.com/en-us/forums/intel-integrated-performance-primitives)



# MULTI-THREADING & HETEROGENEOUS COMPUTING MADE EASY

Intel® Threading Building Blocks (Intel® TBB)

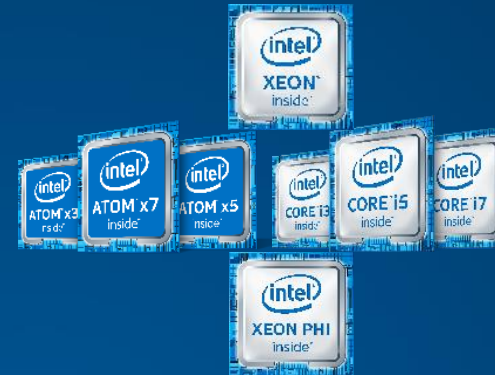
## What is Intel® TBB?

A highly templated C++ library designed to simplify adding parallelism to your application by taking advantage of all the CPU's on a single device or across multiple devices (heterogeneity).

## Why use Intel® TBB?

- High Performance
- Easy to use API's
- Faster Time To Market
- Production Ready

Optimized for



Supports



Addresses



## How to get Intel® TBB

[Intel System Studio](#)  
[Intel Parallel Studio XE](#)  
[Free Tools Program](#)  
[Open Source site](#)

## Applications

- Artificial Intelligence & Automation
- Image processing
- Any solution needing sophisticated threading

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Find out more at: [software.intel.com/intel-tbb](http://software.intel.com/intel-tbb)  
Contact us: [software.intel.com/forums/intel-threading-building-blocks](http://software.intel.com/forums/intel-threading-building-blocks)



# FASTER, SCALABLE CODE, FASTER

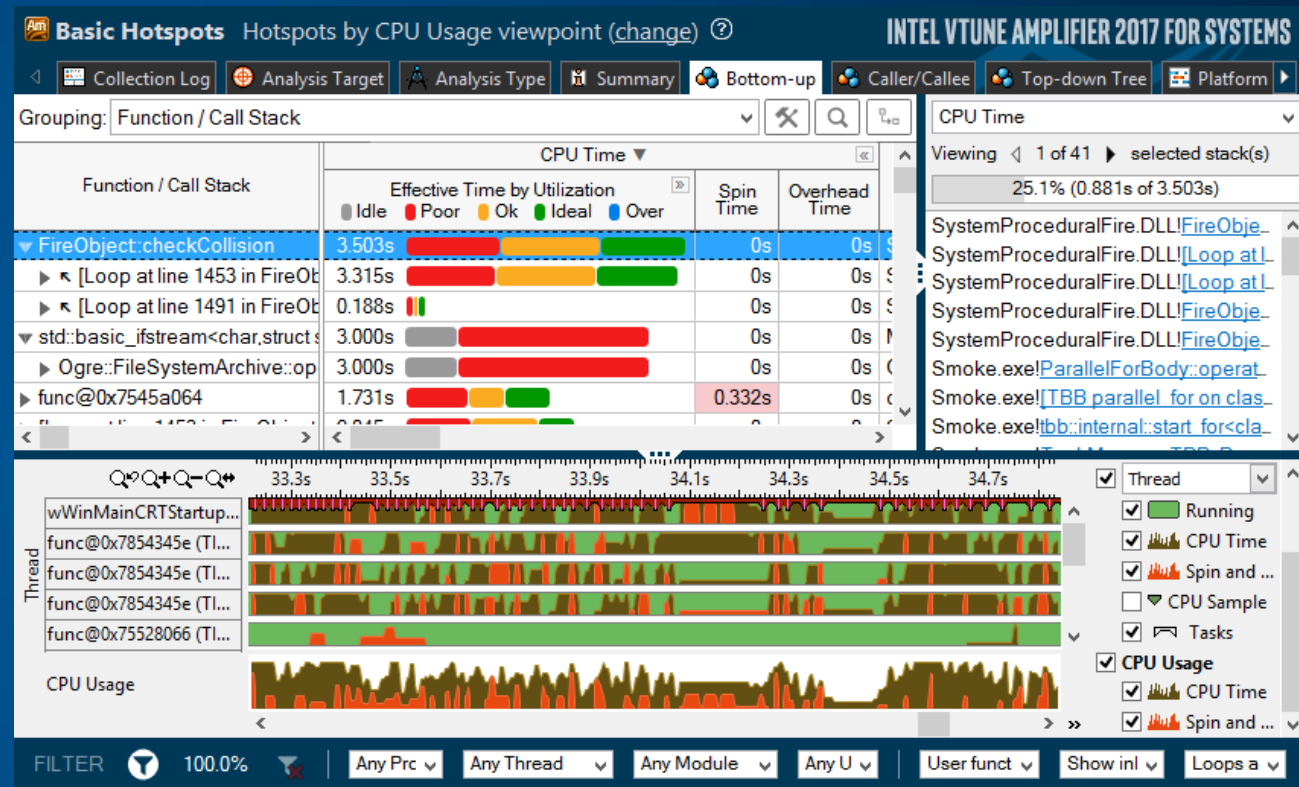
## Intel® VTune™ Amplifier Performance Profiler

### Get Faster Code Faster with Accurate Data & Meaningful Analysis

- Accurate CPU, GPU & threading data
- Memory access & storage analysis
- Powerful data analysis & filtering
- Data displayed on the source code
- Easy set-up, no special compiles

“Last week, Intel® VTune™ Amplifier helped us find almost **3X performance improvement**. This week it helped us improve the performance another **3X.**”

Claire Cates  
Principal Developer  
**SAS Institute Inc.**



Learn More: [intel.ly/vtune-amplifier-xe](https://intel.ly/vtune-amplifier-xe)

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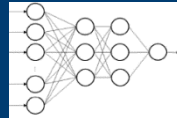
# A NEW SDK FOR COMPUTER VISION

## INTEL® COMPUTER VISION SDK BETA

### MACHINE LEARNING INFERENCE

#### Model Optimizer

- >OpenVX code generation
- >IR->Inference Engine SDK
- Same interface for CPU, GPU, ...



### OPTIMIZED LIBRARIES

OpenVX™  
OpenCV

### UTILITIES

Vision  
Algorithm  
Designer

Many Topologies: Lenet, Alexnet, Googlenet, VGG, SSD + YOLO (soon)

Traditional Computer Vision

OpenCL  
implementations

Prototype

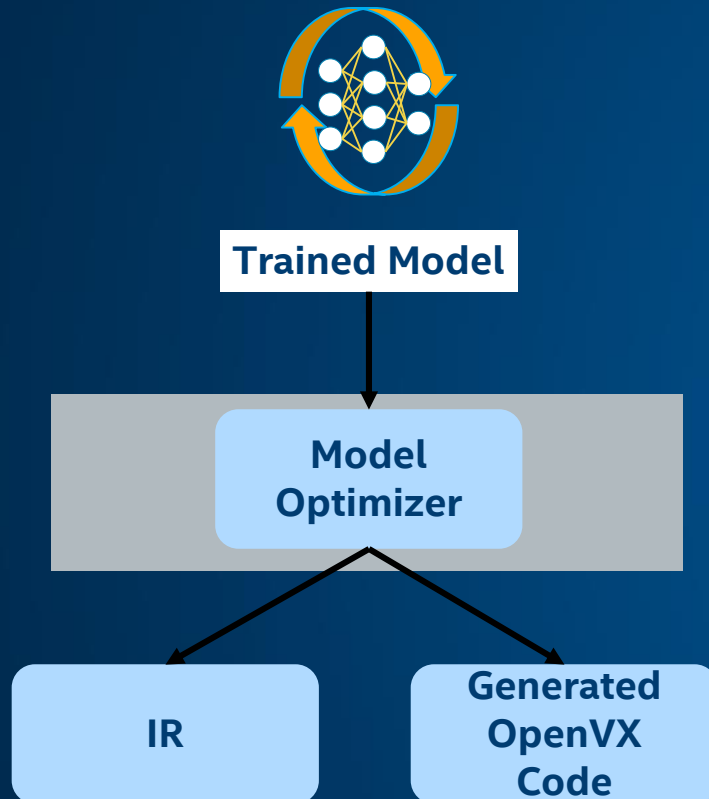
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# MODEL OPTIMIZER



## Model Optimizer Bridges Train and Deploy

- Generate **OpenVX code**
- Generate **Intermediate Representation (IR)**
- Optimize the network
  - Node fusion
  - Node merging
  - Batch normalization
- Calculate and dump the normalized and converted weights/biases (**normalization factor** can be supplied by user if learning phase is skipped)

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# RUN MODEL OPTIMIZER

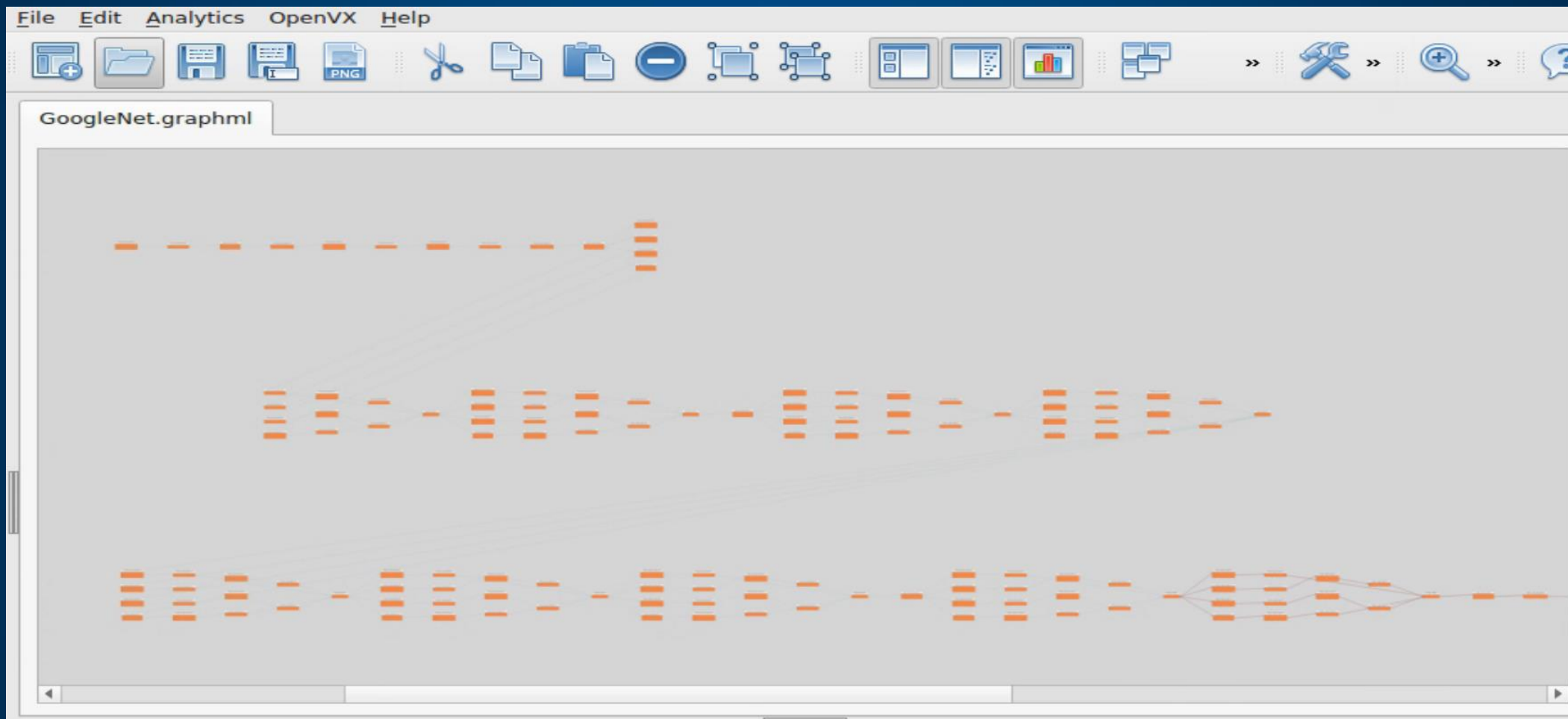
```
$ cd /opt/intel/computer_vision_sdk_2017.0.090
$ source bin/setupvars.sh
$ cd mo/bin
```

```
$ export FRAMEWORK_HOME=
/home/user/Desktop/MO_LAB/caffe/build/lib/
```

```
$ ./ModelOptimizer --target APLK -i \
-d /home/user/Downloads/caffe-
master/models/bvlc_reference_caffenet/deploy.proto
txt \
-w /home/user/Downloads/caffe-
master/models/bvlc_reference_caffenet/bvlc_referen
ce_caffenet.caffemodel \
-f 1 \
-p FP16 \
-o artifacts
```

```
Start working...
Framework plugin: CAFFE
Target type: APLK
Network type: CLASSIFICATION
Batch size: 8
Precision: FP16
Layer fusion: true
Output directory: artifacts
Custom kernels directory:
Network input normalization: 1
Writing binary data to:
artifacts/CaffeNet/CaffeNet.bin
```

# MODEL OPTIMIZER GENERATED OPENVX CODE



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# SETUP CODE

```
// Plugin name (MKLDNNPlugin | CLDNNPlugin | OVXPlugin)
string pluginName = "cLDNNPlugin";
string pluginPath = "path/to/plugin/"+pluginName;

// Load plugin
InferenceEnginePluginPtr plugin(pluginPath);
InferenceEngine::InferenceEnginePluginPtr _plugin(plugin);

// Read network
InferenceEngine::CNNNetReader network;
network.ReadNetwork(pathToModel + ".xml");
network.ReadWeights(pathToModel + ".bin");

InputsDataMap inputs = network.getNetwork().getInputs();

// Allocate input and output blobs here

// Load model to plugin
sts = _plugin->LoadNetwork(network.getNetwork(), &dsc);
```

select cLDNN  
implementation

select IR input files

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# SIMPLE CLASSIFICATION CODE

```
Mat frame, frame2;
for (;;) {
    cap >> frame;

    //resize to expected size (in IR .xml)
    resize(frame, frame2, Size(227, 227));

    //run inference
    long unsigned int framesize=
        frame2.rows*frame2.step1();
    ConvertImageToInput(frame2.data,
framesize, *input);

    sts = _plugin->Infer(*input, *output, data
&dsc);

    // check status here...
}
```

```
//get top classifier label
int blobsize=output->size();
float *data=output->data();
float max=0;
int maxidx=0;
for (int i1=0; i1<blobsize; i1++) {
    if (data[i1]>max) {
        max=data[i1];
        maxidx=i1;
    }
}

// do something with classification

imshow( "frame", frame2 );
if (waitKey(30) >= 0) break;
```

Output is an array  
of category  
possibilities

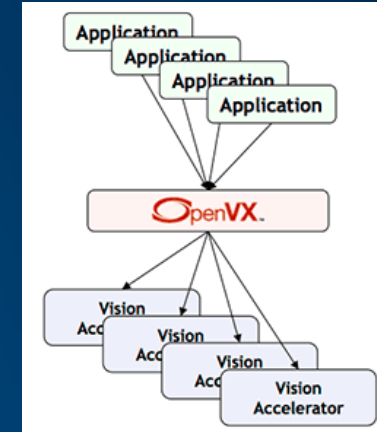
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# OPENCV VS. OPENVX



- Industry standard
  - 47K user community
  - >14M downloads
- Community driven open source
- >2500 algorithms
- CPU C++, growing list of OpenCL/CUDA implementations
- Standard scheduling, no automatic tiling across functions, etc.

- Emerging standard
- Created for power optimized heterogeneous HW development
- Vendor driven, all or partial closed source
- ~50 algorithms
- Designed for fixed function, may be implemented in C++, OpenCL, etc.
- Automatic graph level optimizations (tiling, etc.)

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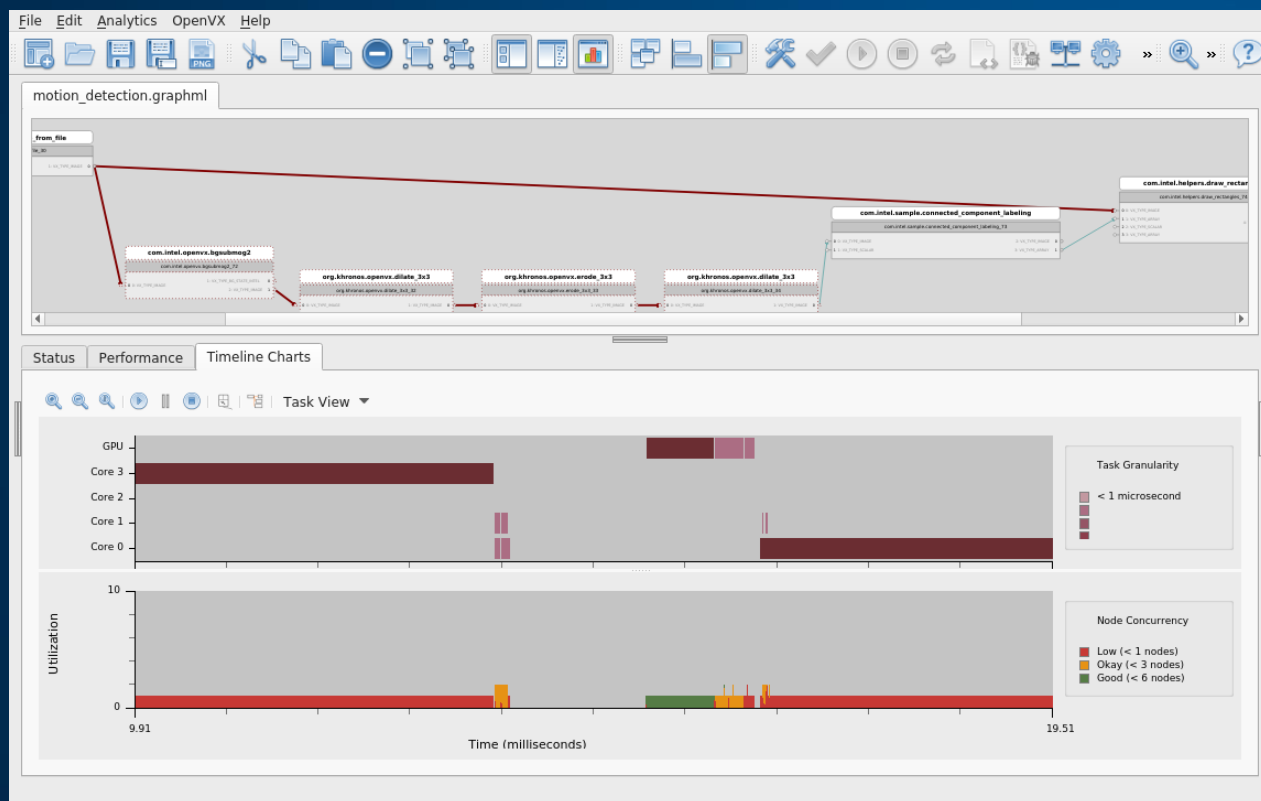
# DEVELOPMENT FLOW OPTIONS

## Vision Algorithm Designer

- Build Pipelines
- Debug
- Performance feedback

## OpenVX™ C/C++ API

- Use with familiar IDEs
- Interoperable with other libraries, SDKs & programming models



```
vx_context context = vxCreateContext();
vx_image input = vxCreateImage( context, 640, 480,
VX_DF_IMAGE_U8 );
vx_image output = vxCreateImage( context, 640, 480,
VX_DF_IMAGE_U8 );

vx_graph graph = vxCreateGraph( context );
vx_image intermediate = vxCreateVirtualImage( graph,
640, 480, VX_DF_IMAGE_U8 );
vx_node F1 = vxF1Node( graph, input, intermediate );
vx_node F2 = vxF2Node( graph, intermediate, output );

vxVerifyGraph( graph );
vxProcessGraph( graph ); // run in a loop
```

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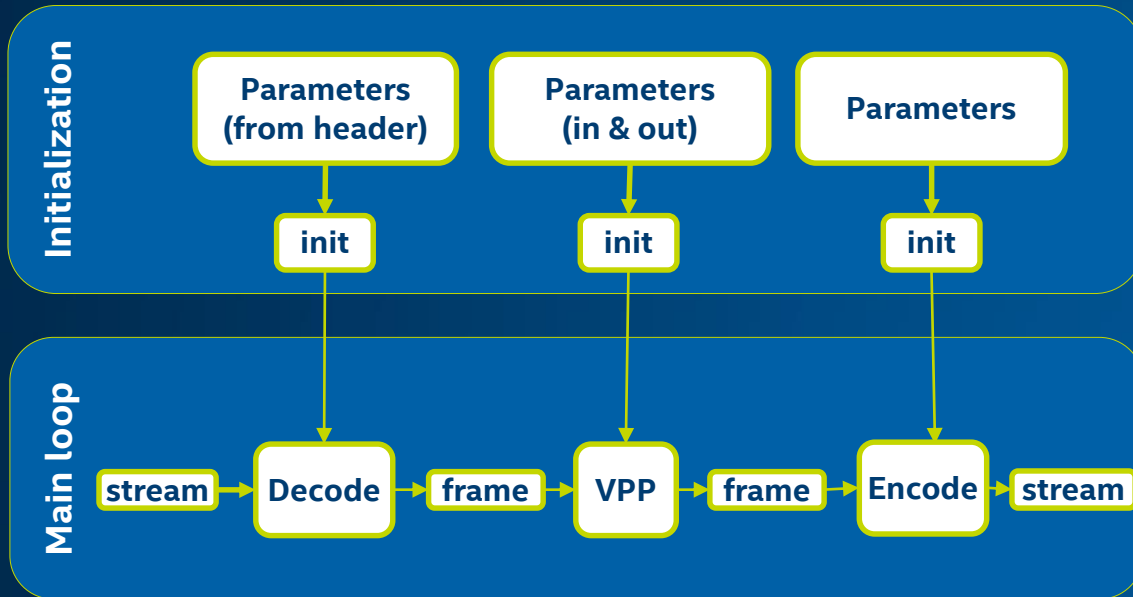
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# THEORY OF OPERATION: INTEL® MEDIA SDK / INTEL® MEDIA SERVER STUDIO



Media accelerator framework  
Codec based  
High level/parameter interface  
3 operations

Good option for:

- Accelerated video encode, decode
- (and short list of frame processing)

More Information

- [Media Server Studio](#)
- [Media SDK](#)
- [Intel Media Code Samples](#)

Out of scope:

\* audio, containers, networking...

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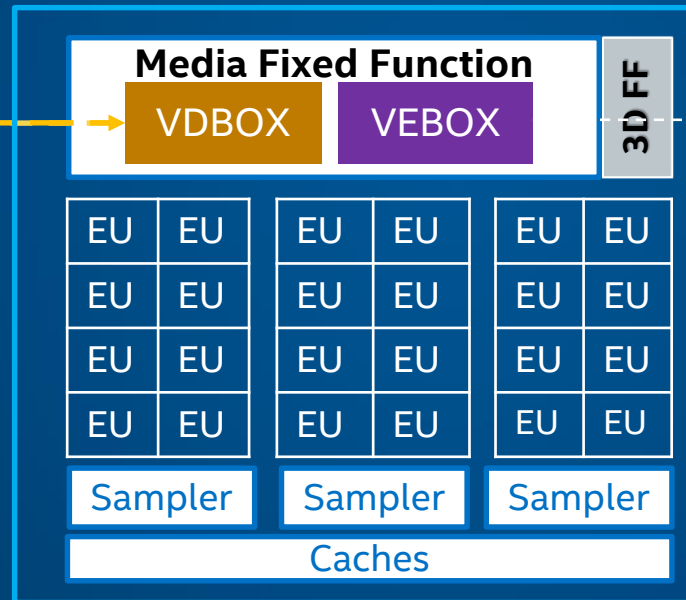
# CODECS + FRAME PROCESSING USE FIXED FUNCTION + EUS

## Video Encoding

ENC= EU+VDBox VME (MB type, motion vectors, bit budget/BRC)  
PAK = VDBox (residue packing & entropy coding)  
VDENC = low power encode (6<sup>th</sup> Generation Core® & forward)

## Video Decoding

BSD=VDBox decode



VPP

## VPHal

Video Processing Hardware Acceleration Layer

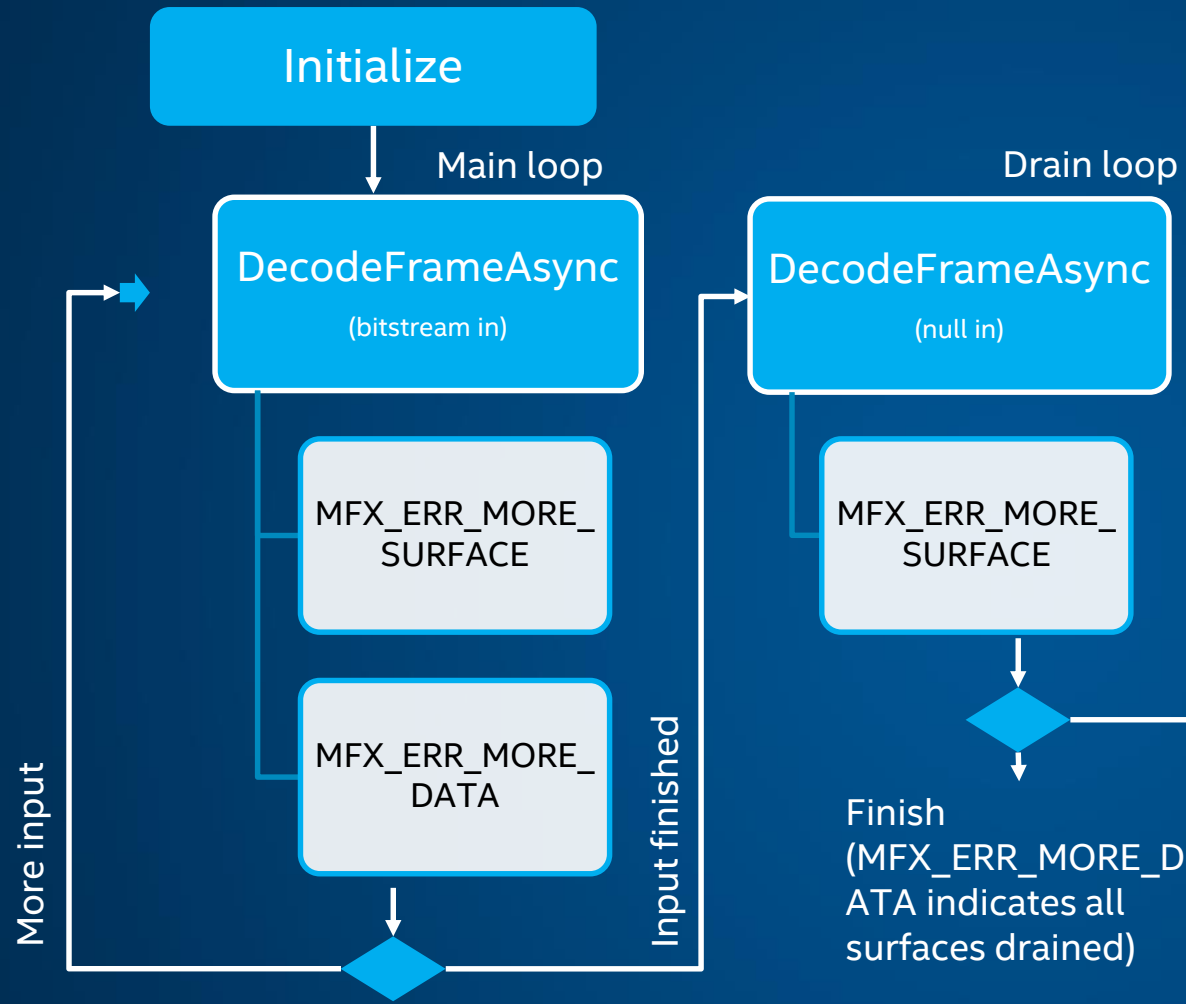
### VEBox

- Deinterlacing
- Denoise (Luma/Chroma)
- Frame Rate Conversion
- Color space conversions
- Composition/alpha blending
- Scaling

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# BASIC DECODE FLOW



## Expected Return Codes for DecodeFrameAsync

### MFX\_ERR\_MORE\_SURFACE

- A new surface is required to proceed – this is where decode will write its output

### MFX\_ERR\_MORE\_DATA

- More input bitstream data is required to proceed

### MFX\_WRN\_DEVICE\_BUSY

- Hardware device is unable to respond. This is an expected output for normal operation and should clear after a short wait. However, if this state persists more than a few milliseconds this may indicate a problem.

### MFX\_WRN\_VIDEO\_PARAM\_CHANGED

- The SDK decoder parsed a new sequence header. Decoding can continue with existing frame buffers. The application can optionally retrieve new video parameters by calling MFXVideoDECODE\_GetVideoParam.

### Other

- Other error codes may be bugs. Please contact an Intel support representative for more info.

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# DECODE

```
do {  
    if (still_reading_file) { // main loop  
        sts = mfxDEC.DecodeFrameAsync(&mfxBS, pmfxSurfaces[nIndex], &pmfxOutSurface, &syncp);  
    }  
    else { // drain loop  
        sts = mfxDEC.DecodeFrameAsync(NULL, pmfxSurfaces[nIndex], &pmfxOutSurface, &syncp);  
        if (sts==MFX_ERR_MORE_DATA) break;  
    }  
  
    switch (sts)  
    {  
    case MFX_WRN_DEVICE_BUSY:  
        MSDK_SLEEP(1); // Wait if device is busy, then repeat  
        break;  
    case MFX_ERR_MORE_SURFACE:  
        nIndex = GetFreeSurfaceIndex(pmfxSurfaces, numSurfaces); // Find free frame surface  
        break;  
    case MFX_ERR_MORE_DATA:  
        readsts = ReadBitStreamData(&mfxBS, fSource); // Read more data  
        if (readsts!=MFX_ERR_NONE) still_reading_file=0;  
        break;  
    }  
    if (MFX_ERR_NONE!=sts) continue;  
  
    sts = session.SyncOperation(syncp, 60000); // Wait until decode finished  
  
    // frame data can be used by application now  
} while (true);
```

Add VPP resize  
and  
classification  
code here

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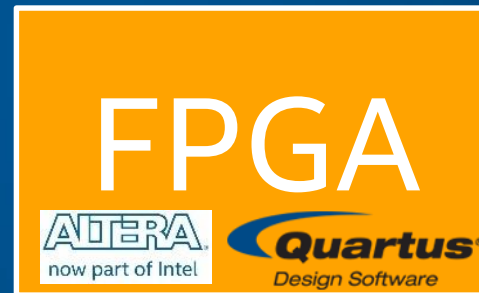
# INTEL® SDK FOR OPENCL™ APPLICATIONS

## SDK Tools



- Kernel analyzer
- Kernel debugger
- Offline compiler
- IDE integration

Implementations for many Intel Hardware platforms



Download from  
[software.intel.com/intel-opencl](https://software.intel.com/intel-opencl)

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# EXTENSIONS MAP

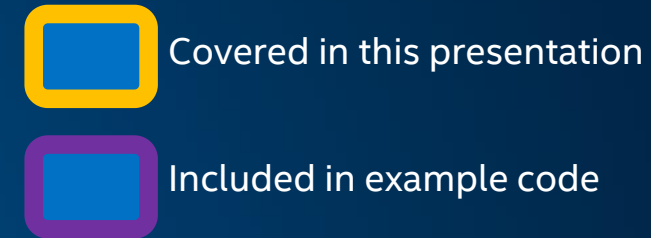


Image from buffer  
mipmap  
depth images  
packed/planar YUV

Intel Subgroups  
Khronos Subgroups  
Short Subgroups

VAAPI  
DX9  
DX11  
OpenGL

SPIR  
Diagnostics  
fp16/fp64  
atomics  
...

For more info: [software.intel.com/articles/opencl-intel-graphics-extensions](https://software.intel.com/articles/opencl-intel-graphics-extensions)

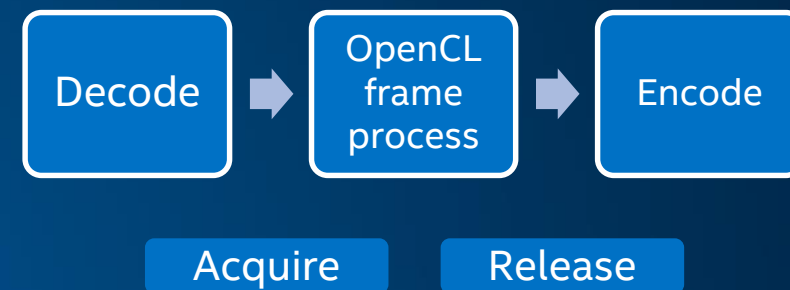
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# SHARING APIS IN ACTION



For more info: [software.intel.com/articles/tutorial-opencl-interopability-with-video-acceleration-api-on-linux-os](https://software.intel.com/articles/tutorial-opencl-interopability-with-video-acceleration-api-on-linux-os)

Interop example code (in this tutorial)

Intel® Media SDK/Intel® Media Server Studio samples (sample\_multi\_transcode, sample\_encode)

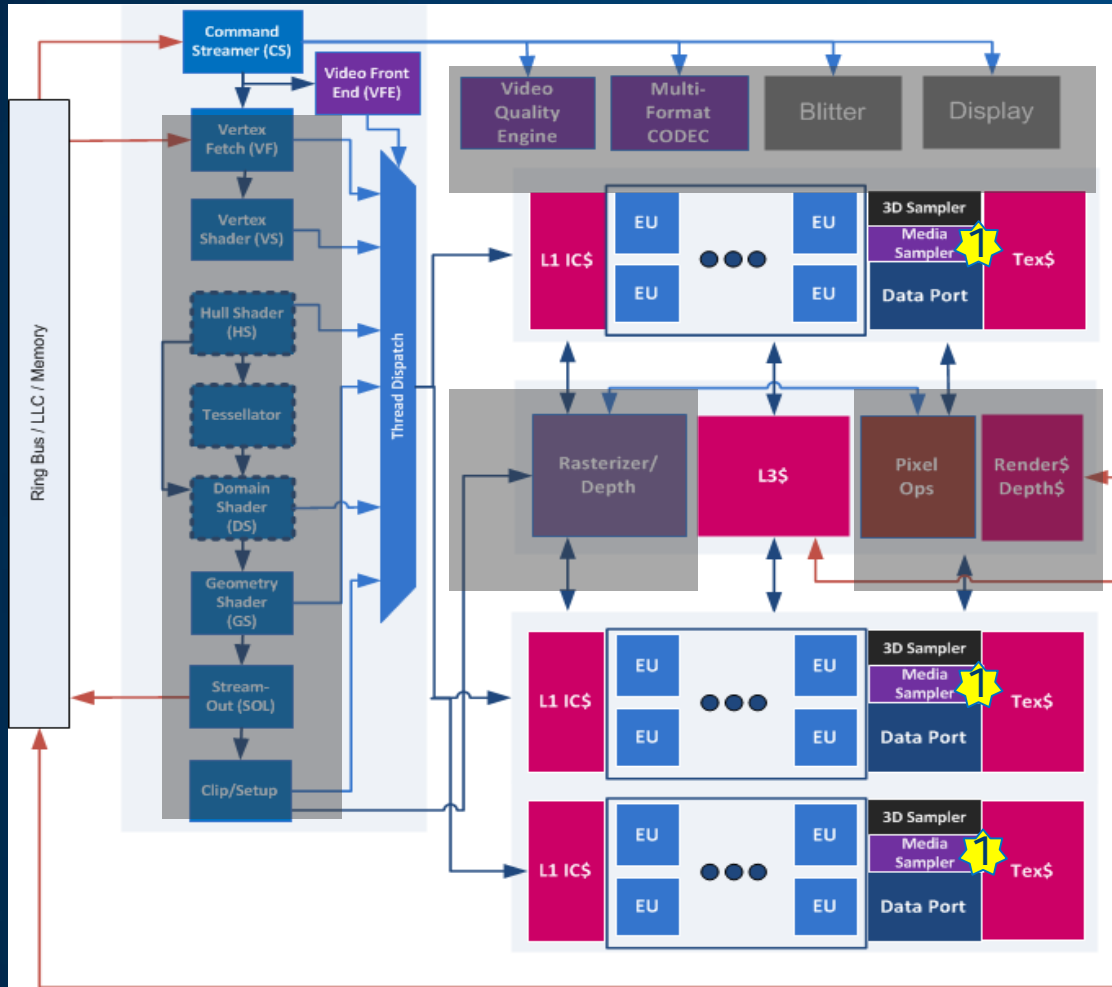
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# HARDWARE VIDEO MOTION ESTIMATION



VME is part of the *Media Sampler*

- Programmable through EUs
- Operates on 16x16 macroblocks
- 1 per sub-slice
  - 2 sub-units (co-issuable)

- Implements key motion estimation operations
  - Inter Motion Estimation
  - Sub-pixel refinement
  - Intra Prediction
  - Many more...
- Programmable general purpose operations

- Optimized for memory bandwidth
- Provides configurable raw compute
- Smarts in the hands of the programmer

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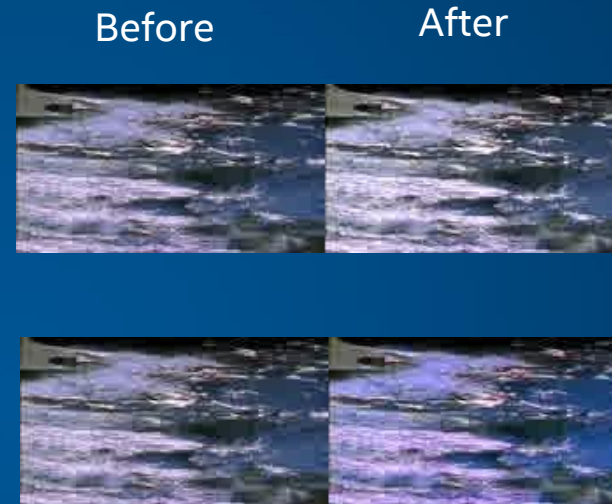
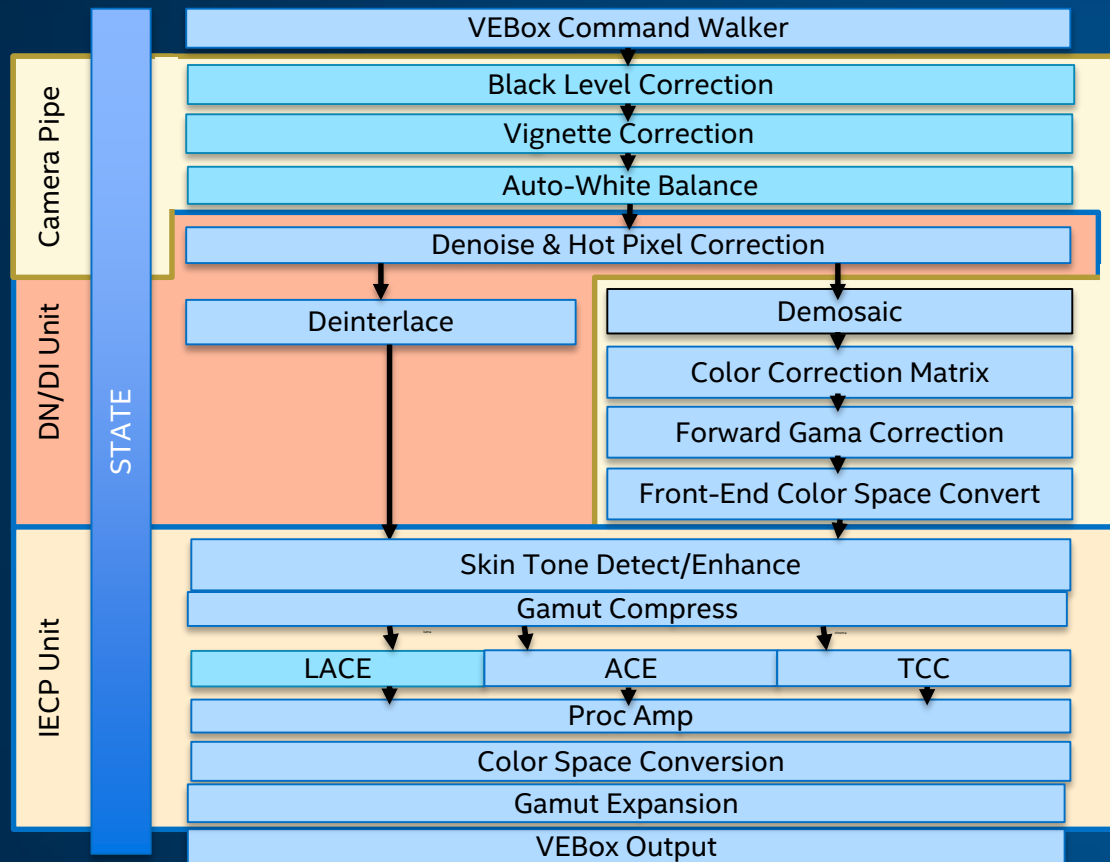




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# INTRODUCING VEBOX



Adaptive Contrast Enhancement (ACE)

Total Color Correction (TCC)

A configurable pipeline of common video processing operations

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**NEW!**

## INTEGRATE VISUAL UNDERSTANDING

Intel® Computer Vision SDK Beta

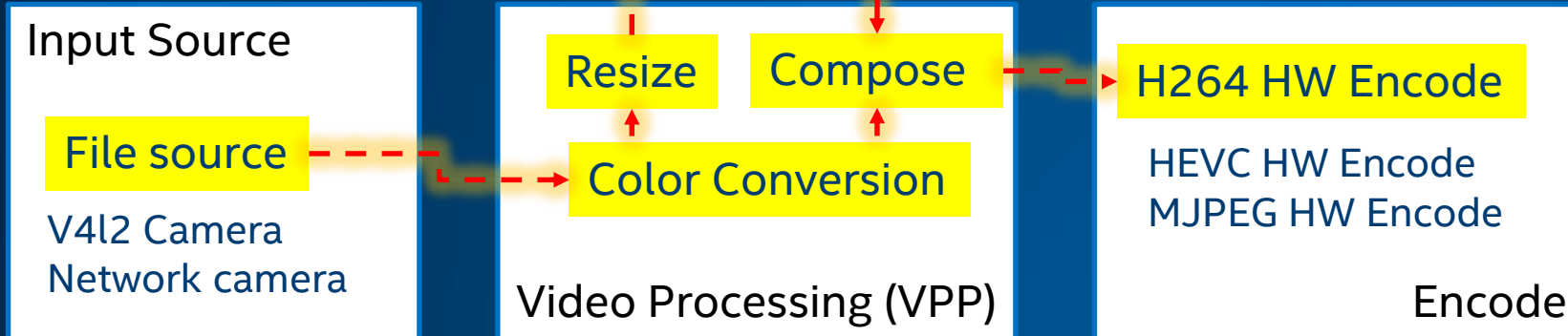
OpenVX/CNN

SSD CNN OpenVX Graph

clDNN plugin

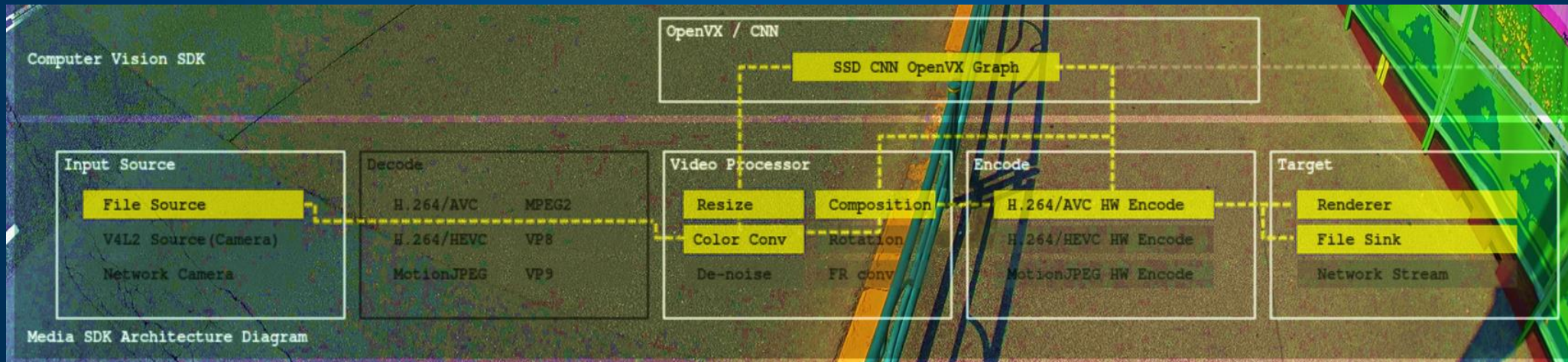
## HETEROGENEOUS CUSTOM DEVELOPMENT

Intel® SDK for OpenCL™  
Application Development



## ACCELERATE VIDEO PROCESSING

Intel® Media SDK

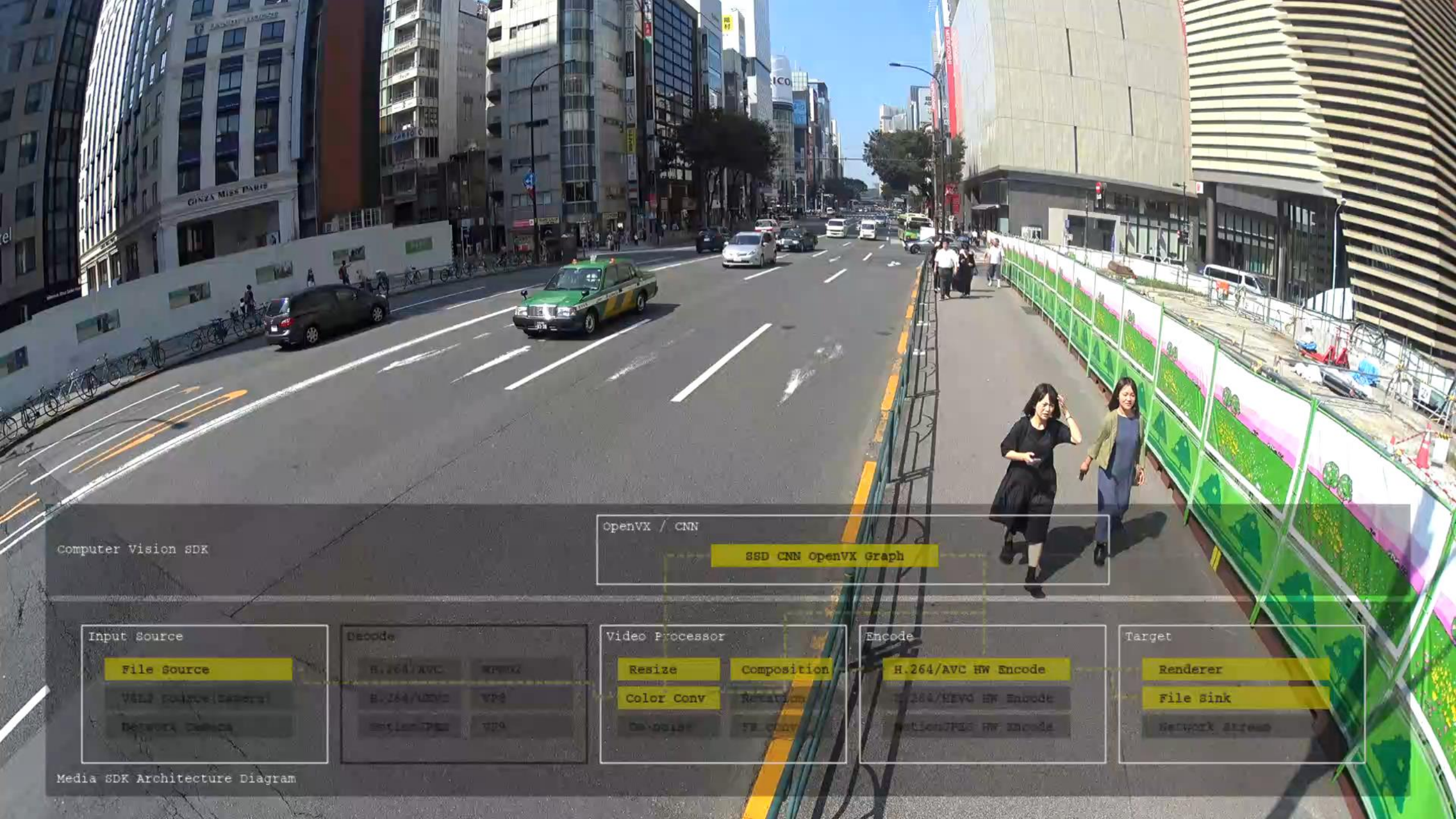


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Computer Vision SDK

OpenVX / CNN

SSD CNN OpenVX Graph

Input Source

File Source

V4L2 Device (Camera)

Device Source

Decode

H.264/AVC VP8

H.264/HEVC VP9

HEVC/VP9 VP9

Video Processor

Resize

Color Conv

De-Noise

Composition

Rotation

FE Conv

Encode

H.264/AVC HW Encode

H.264/HEVC HW Encode

HEVC/VP9 HW Encode

Target

Renderer

File Sink

Network Stream

# MORE RESOURCES – DOWNLOAD SOFTWARE TO GET STARTED

## ACCELERATE VIDEO PROCESSING

Intel® Media SDK

Free Download >  
[software.intel.com/media-sdk](https://software.intel.com/media-sdk)

## INTEGRATE VISUAL UNDERSTANDING

**NEW!** Intel® Computer Vision SDK Beta

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## CUSTOMIZE WITH OPENCL Intel® SDK for OpenCL™ Applications

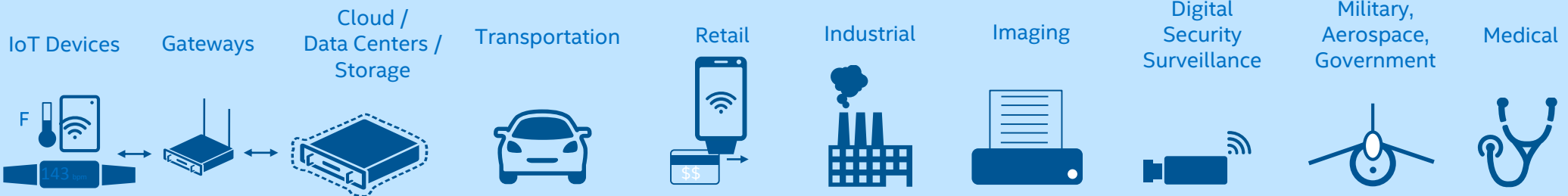
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← CPU + system optimization

↑ Specialized hardware acceleration ↑



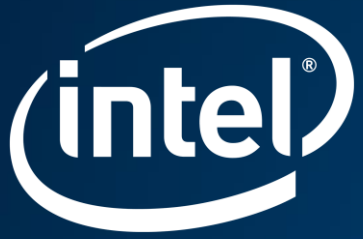
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**THANK YOU!**

# Empowering Product Creators to Harness Embedded Vision



The Embedded Vision Alliance ([www.Embedded-Vision.com](http://www.Embedded-Vision.com)) is a partnership of 60+ leading embedded vision technology and services suppliers

Mission: Inspire and empower product creators to incorporate visual intelligence into their products

The Alliance provides low-cost, high-quality technical educational resources for product developers

Register for updates at [www.Embedded-Vision.com](http://www.Embedded-Vision.com)

The Alliance enables vision technology providers to grow their businesses through leads, ecosystem partnerships, and insights

For membership, email us: [membership@Embedded-Vision.com](mailto:membership@Embedded-Vision.com)



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